



1694/PVD/DV
7828.5180

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of
HEYDER et al.)
Serial No.: 08/901,485)
Filing Date: July 28, 1997)
For: MULTIPLE LOADLOCK SYSTEM)

Group Art Unit:
Examiner:

COPY

DECLARATION OF THOMAS B. BREZOCZKY

I, Thomas B. Brezoczky, a co-inventor of the above-identified application, hereby declare and state as follows:

Based on information and belief, it is my understanding that semiconductor wafer handling systems which transferred a stack of wafers simultaneously from one cassette to another cassette in atmosphere, were known and used prior to July 28, 1996, which is more than one year prior to July 28, 1997, the filing date of the present application.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and, further, that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Sec. 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patents issued thereon.

Dated: 1/29/99

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